

# Linux Foundation RVFA

LINUX FOUNDATION RISC-V FOUNDATIONAL CERTIFICATION QUESTIONS & ANSWERS

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Exam Summary – Syllabus – Questions

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**RVFA**

[RISC-V Foundational Associate \(RVFA\)](#)

60 Questions Exam – 75% Cut Score – Duration of 90 minutes

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## Know Your RVFA Certification Well:

The RVFA is best suitable for candidates who want to gain knowledge in the Linux Foundation IoT & Embedded Development. Before you start your RVFA preparation you may struggle to get all the crucial RISC-V Foundational materials like RVFA syllabus, sample questions, study guide.

But don't worry the RVFA PDF is here to help you prepare in a stress free manner.

The PDF is a combination of all your queries like-

- What is in the RVFA syllabus?
- How many questions are there in the RVFA exam?
- Which Practice test would help me to pass the RVFA exam at the first attempt?

Passing the RVFA exam makes you RISC-V Foundational Associate (RVFA). Having the RISC-V Foundational certification opens multiple opportunities for you. You can grab a new job, get a higher salary or simply get recognition within your current organization.

## Linux Foundation RVFA RISC-V Foundational Certification Details:

<b>Exam Name</b>	Linux Foundation RISC-V Foundational Associate (RISC-V Foundational)
<b>Exam Code</b>	RVFA
<b>Exam Price</b>	\$250 USD
<b>Duration</b>	90 minutes
<b>Number of Questions</b>	60
<b>Passing Score</b>	75%
<b>Recommended Training / Books</b>	<a href="#">RISC-V Fundamentals (LFD210)</a>
<b>Schedule Exam</b>	<a href="#">The Linux Foundation Training &amp; Certification</a>
<b>Sample Questions</b>	<a href="#">Linux Foundation RVFA Sample Questions</a>
<b>Recommended Practice</b>	<a href="#">RISC-V Foundational Associate (RVFA) Practice Test</a>

## RVFA Syllabus:

Section	Objectives	Weight
<b>RISC-V Overview</b>	<ul style="list-style-type: none"> <li>- History of RISC-V: The Free and Open ISA</li> <li>- RISC-V International</li> <li>- RISC-V Documentation</li> <li>- Contribute to RISC-V</li> </ul>	10%
<b>RISC-V Instruction Set Architecture</b>	<ul style="list-style-type: none"> <li>- RV32I and RV64I</li> <li>- Understand Instruction Formats: branching, accessing memory, and accessing data structures</li> <li>- Understand the modularity of RISC-V as an ISA: core ratified (M, C, F, D, A) and other extensions</li> <li>- Understand Privilege Modes, system calls, CSRs, exceptions, and interrupt handling</li> <li>- Understand memory model, cache management, and virtual memory management</li> </ul>	35%
<b>Assembly Language for RISC-V</b>	<ul style="list-style-type: none"> <li>- Understand RISC-V specific assembly language syntax and features, including CSR access</li> <li>- Write and debug RISC-V assembly code</li> <li>- Assess performance of assembly code</li> <li>- Convert high-level code to assembly code</li> </ul>	25%
<b>High Level Languages for RISC-V: C Programming</b>	<ul style="list-style-type: none"> <li>- Understand RISC-V tools including compilers, debuggers, simulators, performance tools, OSes, and SDKs</li> <li>- Understand calling conventions (ABIs), the stack, and disassembly</li> <li>- Understand inline assembly</li> </ul>	15%
<b>RISC-V Operating Systems &amp; Tools</b>	<ul style="list-style-type: none"> <li>- Fundamentals of Operating Systems including implementing basic OS functionality in RISC-V ASM</li> <li>- Understanding basic use and functionality of firmware for RISC-V platforms</li> <li>- Understanding microcontrollers versus application processors</li> <li>- Running RISC-V Applications in a General Purpose OS</li> </ul>	15%

# Linux Foundation RVFA Sample Questions:

## Question: 1

Disassembly in RISC-V C programming helps in:

- a) Converting assembly code to high-level language
- b) Network configuration
- c) Website optimization
- d) Understanding the compiled code's machine instructions

**Answer: d**

## Question: 2

In RISC-V, a microcontroller differs from an application processor in terms of:

- a) Graphic design capabilities
- b) Computing power and application scope
- c) Website development
- d) Social media management

**Answer: b**

## Question: 3

Performance tools in RISC-V C programming are used to:

- a) Improve website traffic
- b) Enhance code efficiency and speed
- c) Design user interfaces
- d) Manage online content

**Answer: b**

## Question: 4

What are ABIs in the context of RISC-V C programming?

- a) Application Binary Interfaces
- b) Automated Banking Interfaces
- c) Audio Broadcasting Interfaces
- d) Artificial Bot Intelligence

**Answer: a**

**Question: 5**

Which of the following is true about RISC-V assembly language?

- a) It's identical to ARM assembly
- b) It does not support loops
- c) It has unique syntax and features
- d) It's primarily for database manipulation

**Answer: c**

**Question: 6**

Running RISC-V applications in a General-Purpose OS involves:

- a) Enhancing website aesthetics
- b) Compatibility and integration with the OS
- c) Graphic design
- d) E-commerce management

**Answer: b**

**Question: 7**

A key difference between microcontrollers and application processors in RISC-V is:

- a) Their role in social media
- b) The complexity of tasks they handle
- c) Their use in graphic design
- d) Their ability to manage finances

**Answer: b**

**Question: 8**

Understanding calling conventions in RISC-V C programming is crucial for:

- a) Graphic design
- b) Web development
- c) Function invocation and argument passing
- d) Data analysis

**Answer: c**

**Question: 9**

Who can contribute to the development of RISC-V?

- a) Only selected corporations
- b) Only academic institutions
- c) Only RISC-V International members
- d) Any interested party

**Answer: d**

**Question: 10**

What is a fundamental aspect of Operating Systems in the context of RISC-V?

- a) Implementing basic OS functionality in RISC-V ASM
- b) Developing web applications
- c) Creating graphic designs
- d) Managing corporate finances

**Answer: a**

## Study Guide to Crack Linux Foundation RISC-V Foundational RVFA Exam:

- Getting details of the RVFA syllabus, is the first step of a study plan. This pdf is going to be of ultimate help. Completion of the syllabus is must to pass the RVFA exam.
- Making a schedule is vital. A structured method of preparation leads to success. A candidate must plan his schedule and follow it rigorously to attain success.
- Joining the Linux Foundation provided training for RVFA exam could be of much help. If there is specific training for the exam, you can discover it from the link above.
- Read from the RVFA sample questions to gain your idea about the actual exam questions. In this PDF useful sample questions are provided to make your exam preparation easy.
- Practicing on RVFA practice tests is must. Continuous practice will make you an expert in all syllabus areas.

### Reliable Online Practice Test for RVFA Certification

Make VMExam.com your best friend during your Linux Foundation RISC-V Foundational Associate exam preparation. We provide authentic practice tests for the RVFA exam. Experts design these online practice tests, so we can offer you an exclusive experience of taking the actual RVFA exam. We guarantee you 100% success in your first exam attempt if you continue practicing regularly. Don't bother if you don't get 100% marks in initial practice exam attempts. Just utilize the result section to know your strengths and weaknesses and prepare according to that until you get 100% with our practice tests. Our evaluation makes you confident, and you can score high in the RVFA exam.

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